JAN 1 3 2003 et No.: GR 98 P 2499 P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Thomas Peter Haneder et al.

Applic. No.

09/801,209

Filed

March 7, 2001

Title

Ferroelectric Transistor, Use Thereof In A Memory Cell Configuration And Method Of Producing The Ferroelectric

Transistor

Art Unit

2814

ASSOCIATE POWER OF ATTORNEY

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

Please recognize MARK P. WEICHSELBAUM (Reg. No. 43,248) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted

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